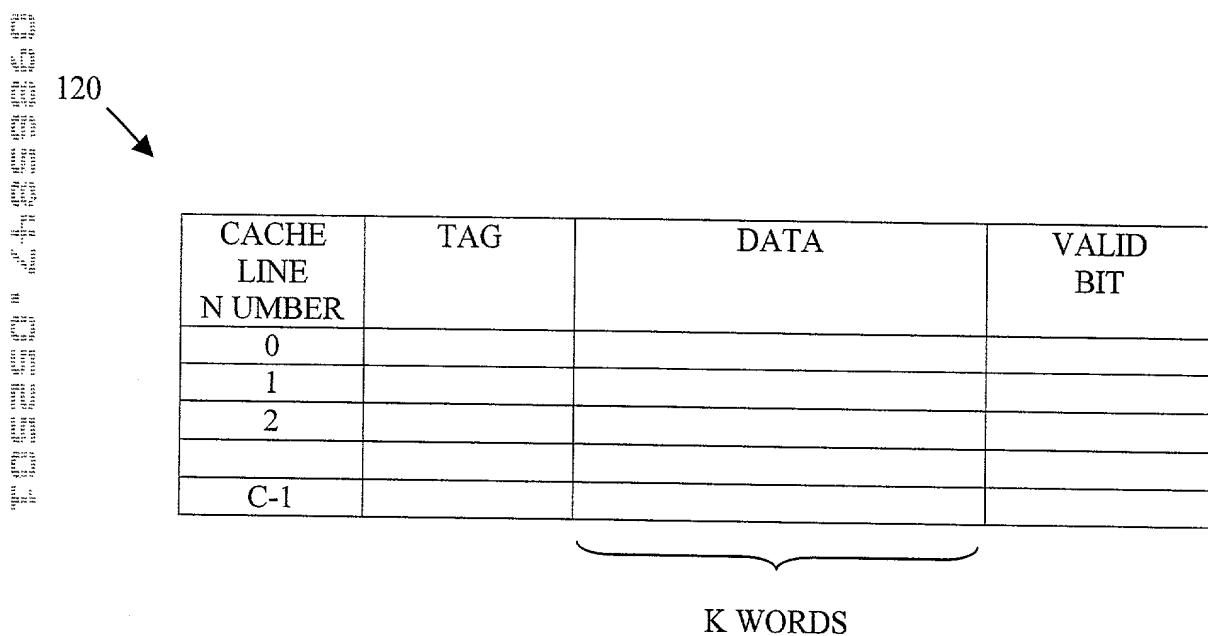


**FIG. 1**  
**PRIOR ART**



**FIG. 2**  
**PRIOR ART**

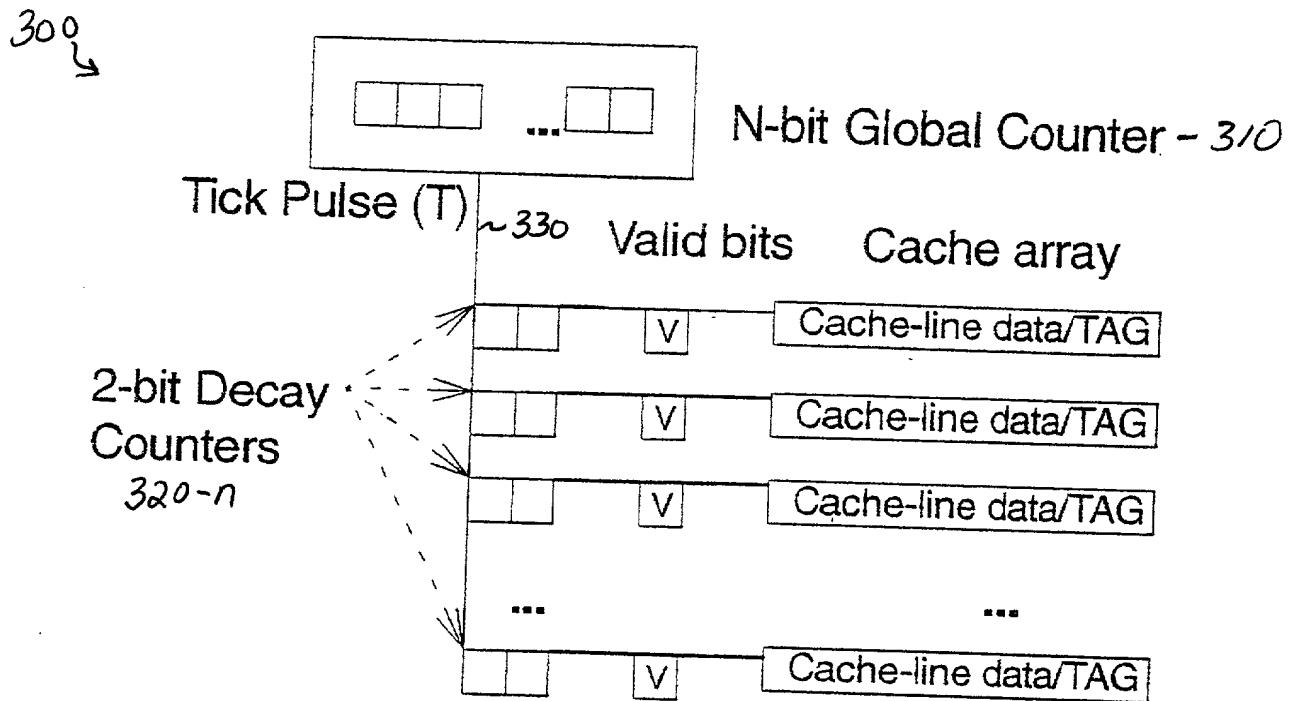


FIG. 3

300  
330  
320-n  
330

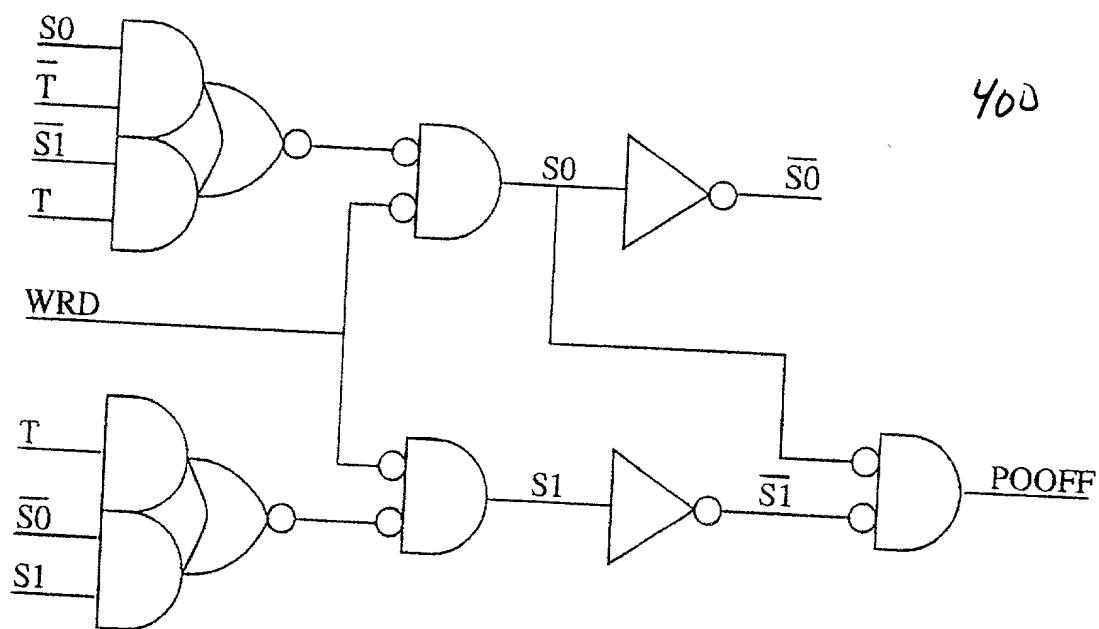
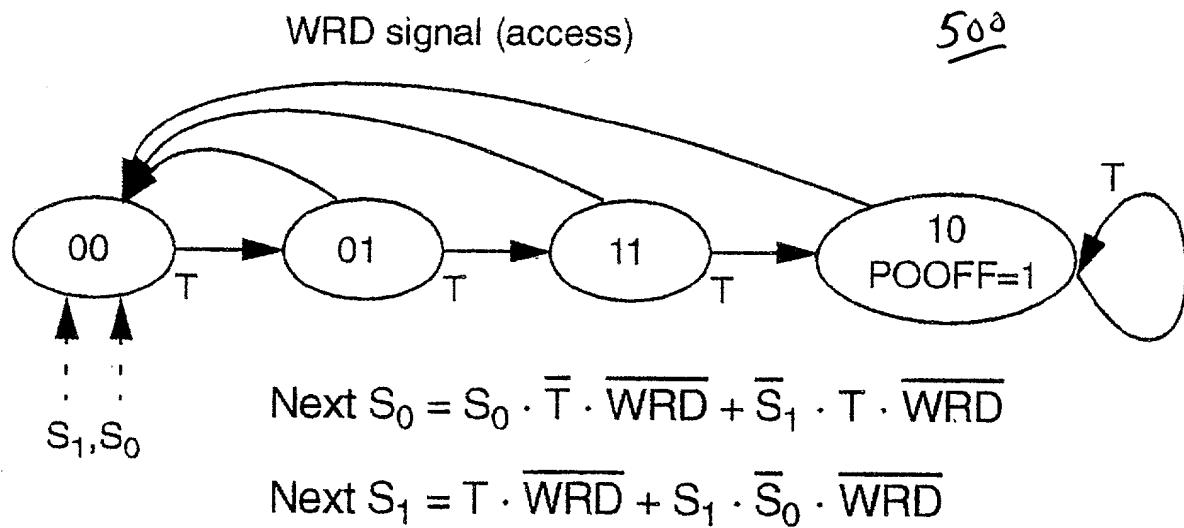
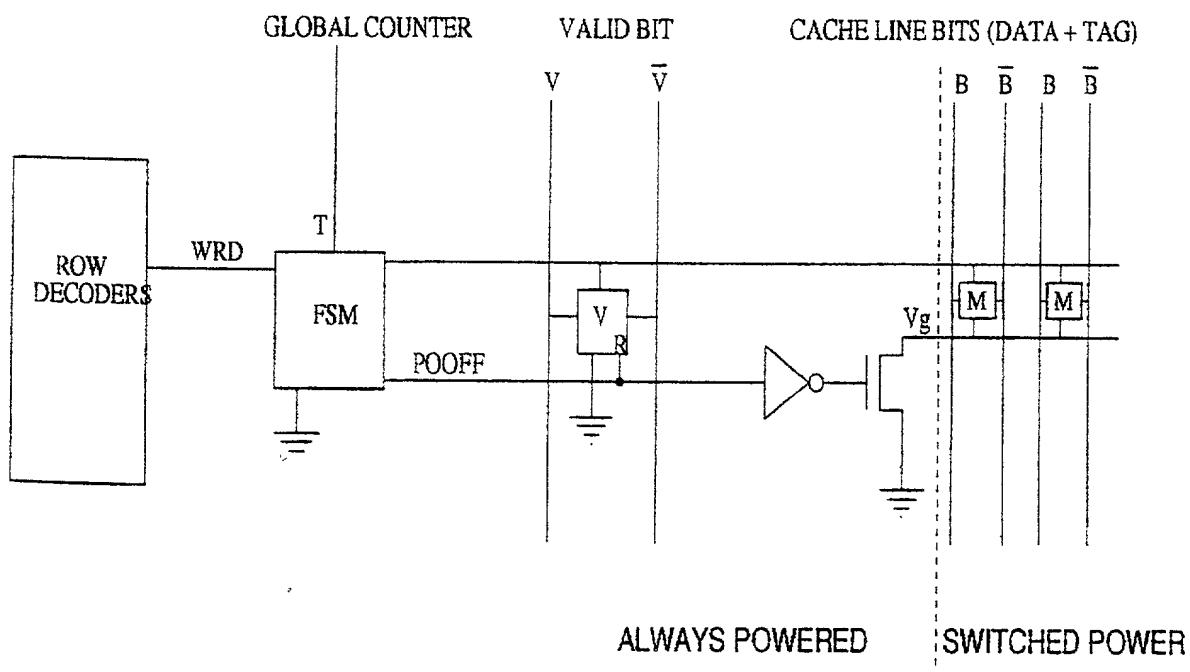


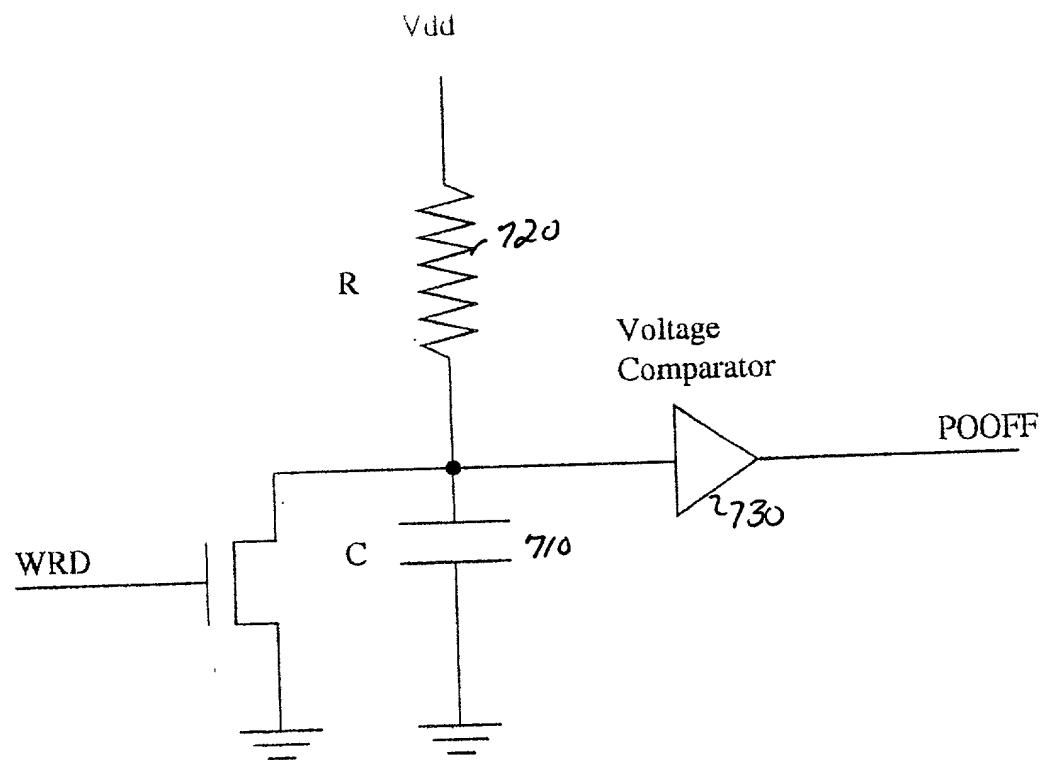
FIG. 4



**FIG. 5**



**FIG. 6**



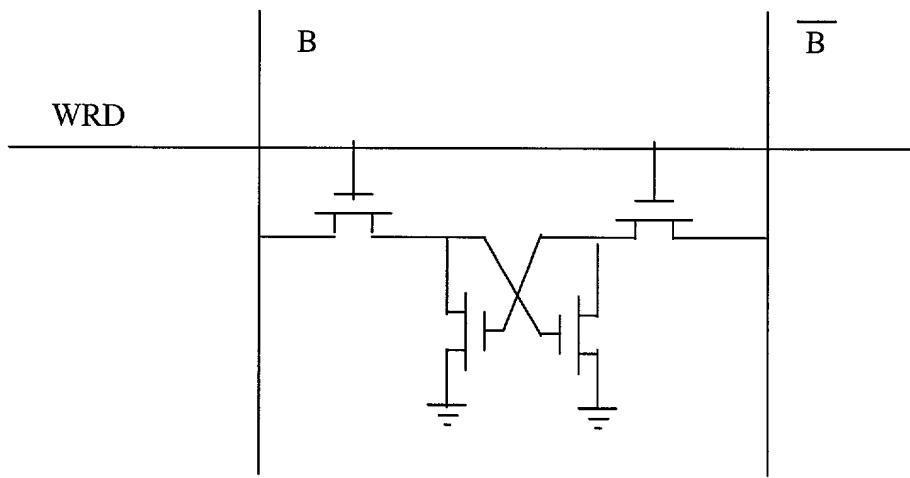
**FIG. 7**

800

CACHE LINE NUMBER	TAG	DATA	VALID BIT	DIRTY BIT
0				
1				
2				
...				
C-1				

K WORDS

**FIG. 8**



**FIG. 9**

